> Load Simulator

SyCTRL FPGA Inside

The Load simulator is used during AIT /AIV phases for representative power consumption of flight equipment and can be based, either on static load (RLC), either on programmable loads (electronic loads).

As a baseline, it provides up to several hundreds of loads with standard over voltage and over current protection (for dynamic loads), based on COTS items.

As options, it can be fitted with:

- Static load power-on status (local or remote)
- Local or remote static load switching (On / Off / Short circuit)
- Individual front panel test points
- Specific sensor simulation (thermistor, cell sample, switch, …) thanks to Clemessy SyCTRL products.

Offers and expertise

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CLEMESSY

> Load Simulator

Offers and expertise

Function

- Controlled and secured electrical load (dynamic or static)
- Over-voltage, over-current, under-voltage, reverse-over-current protection
- Remote and local control mode
- Self test capability
- Safety loop signal management (Inhibit input & Fault output signals)
- Interface cable to spacecraft

Implementation

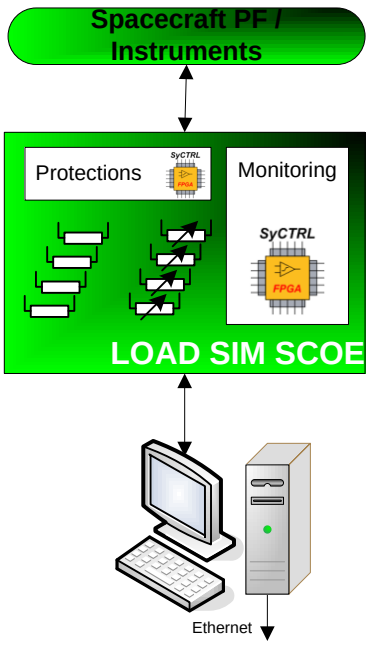
- Static and dynamic loads
- Independent protection / HKTM / TM/TC features based on SyCTRL
- Ethernet TCP/IP interface for remote control
- Windows man machine interface for local control
- 19'' rack integrated.

Performance

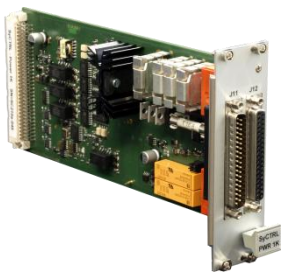
- Number of static channels: No limit
- Number of dynamic channels: No limit
- Protection reaction time: <20 msec

Used technology

- Agilent , AMREL, H&H electronic loads
- Clemessy's SyCTRL second level protection and TM/TC features
- LXI / GPIB interface with electronic loads



SyCTRL FPGA 0 1 0 1 0 1 0 1 0



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